

UAD2^{pro}



The **Universal Access Device 2^{pro}** is the new smart member of the UDE target Access Device family. It replaces the approved UAD2 and offers new enhancements in the well-known form style. It establishes a bridge to the more powerful UAD3+ using the same target adapter solution.

UAD2^{pro} offers fastest target access via JTAG, cJTAG, DAP, DAP over CAN Physical Layer (DXCPL), SPD (Single Pin DAP) via CAN, SWD, ASC and CAN with build-in ESD protection. Accessing your AURIX, TriCore, PowerArchitecture, S32V234, Cortex, C16x, ST10, XC2000, XE166, XMC4500, Arm7, Arm9, Arm11, SuperH SH-2A derivatives is now easier than ever. The unique combination of JTAG and CAN bus, measurements of a mere 8.5 x 13 x 3.5 cm³ (W x D x H), and a robust aluminum housing, predestine the UAD2^{pro} for mobile use in the field.

For debugging microcontroller boards with high-voltage components, as is common for instance with motor or inverter controls, target adapters with an electrical isolation of up to 1,000 VRMS can optionally also be used with the UAD2^{pro}. At the same time, the full transmission bandwidth of 50 MHz serial clock is maintained.

The Universal Access Device 2^{pro} is optimized for High-Speed Communication between the UDE on the Host PC and a target system. UAD2^{pro} supports access features of UDE in an optimized manner.

- General target connector (1,65 - 5,5 Volts I/O ring voltage) supports **JTAG, cJTAG, DAP, DAP over CAN Physical Layer (DXCPL), SPD** via **CAN, SWD** debug communication channel up to 50 MHz shift clock - download rate up to 3.5 MByte/s
- Special target adapter are available for all supported target and interface definitions as TriCore, PowerArchitecture, Cortex, XC2000, XE166, XMC4500, Arm7, Arm9, Arm11, SuperH SH-2A with up to 50 MHz shift clock

- Isolated adapters (RF coupler technology with 1,000 VRMS isolation) - supports full bandwidth 50 MHz shift clock
- Lowest power consumption from target
- Flexible serial high-speed communication to a C16x, ST10, XC2000, XE166, TriCore, PowerArchitecture and Arm/Cortex based target system via a D-Sub connector. The following serial modes are available:
 - Asynchronous serial RS232 interface
 - CAN interface.
- 480 Mbps Host Communication Speed via USB 2.0
- Supported OS: Windows® 7, Windows® 8.1, Windows® 10 (32- and 64-bit)
- Size: Standalone Communication device 8,5 x 13 x 3,5 cm³ (W x D x H w/o connectors).

CAN Bus Analyzer

CAN bus D-Sub male connector (CiA pin assignment) as debugging communication channel to C166, ST10, XC2000, XE166, TriCore, PowerArchitecture and Arm/Cortex based CAN target systems (CAN debugger)

- Independent intelligent subsystem enables continuous trace of CAN bus messages
- CAN bus analyzing may run as background task of debug communication.